

AMENDMENTS

In the Claims

Please delete Claims 9-11 without prejudice, and amend the remaining claims as follows:

1. (currently amended) A card ~~memory device having an input/output device and having~~
~~embedded for insertion in a card reader, wherein:~~
the card is a memory card device having connections forming an input/output means,
there being embedded on the card:
 - a microprocessor;
 - a plurality of memory units for storage of digital data; and
 - a selecting device for selecting one of the memory units and for routing the
address information and data to and/or from the selected memory unit,
~~wherein~~ the microprocessor is being interposed between the input/output
~~device means~~ and the selecting device and is the microprocessor being
adapted for:
passing routing through all data and address information to be sent from to
and ~~received sent~~ by the card ~~memory device~~, and
for supplying address information for which relates to the data sent to each
selected memory unit.

2. (currently amended) The card ~~memory device~~ according to claim 1, wherein the addressing scheme for each memory location of a memory unit includes a parallel and a serial portion.

3. (currently amended) The card ~~memory device~~ according to claim 2, wherein a parallel port of the selecting device is connected to a parallel port of the microprocessor for receiving the parallel portion of a card memory address.

4. (currently amended) The card ~~memory device~~ according to claim 2 ~~or 3~~, wherein a first serial port of the selecting device is connected to a serial port of the microprocessor for receiving the serial portion of a card memory address.

5. (currently amended) The card ~~memory device~~ according to ~~any of~~ claims 2 ~~to 4~~, wherein the selecting device has a plurality of second serial ports, one connected to each memory unit via a serial bus and the parallel address portion defines one of the second serial ports of the selecting device.

6. (currently amended) The card ~~memory device~~ according to ~~any previous~~ claim 1, wherein the memory capacity is at least 1 Mbytes.

7. (currently amended) The card ~~memory device~~ according to ~~any previous~~ claim 1, wherein the ~~input/output device comprises~~ connections are a first set of surface contacts including a ground contact, a power source contact and a data input and/or output contact, and the first set corresponding to a second set of contacts in a the card ~~memory device~~ reader, the ground contact of the first set being arranged to ~~contact~~ ground any ~~one~~ contact ~~of~~ of the second set before this reader contact makes contact with any of the power and/or data contacts.

8. (currently amended) The card ~~memory device~~ according to claim 7, wherein the ground contact on the card ~~memory device~~ surrounds the data contact on three sides.

9. (canceled) A card memory device comprising:

a first set of surface contacts including a ground contact, a power source contact and a data input and/or output contact, the first set corresponding to a second set of contact s in a card memory device reader, the ground contact of the first set being arranged to make contact with any contact of the second set before this reader contact makes contact with any of the power and/or data contacts.

10. (canceled) The card memory device according to claim 9, wherein the ground contact on the card memory device surrounds the data contact on three sides.

11. (canceled) A method of secure operation of a computing device, comprising the use of a first card memory device having a microprocessor and a reader connected to the computing device for reading the first card memory device, the method comprising the steps of:

configuring the operating system of the computing device so that operation is limited

unless the first card memory device is inserted into the reader;

generating one of more codes relating to system information of the computing device;

comparing the generated codes wit other codes in the first card memory device,

the other codes representing validated system data for the computing device; and

only allowing further unrestricted operation of the computing device if the generated and

stored other codes are the same.

12. (canceled) The method according to claim 11, wherein the first card memory device is a card memory device according to any of the claims 1 to 10.

13. (new) The card according to claim 1, wherein the microprocessor is a chip with read only memory on the chip.

14. (new) The card according to claim 13, wherein the microprocessor has on-chip random access memory.

15. (new) The card according to claim 13, wherein the microprocessor has on-chip non-volatile memory.

16. (new) The card according to claim 13, wherein the memory units are external to the microprocessor chip.

17. (new) The card according to claim 13, wherein the selecting device is external to the microprocessor chip.

18. (new) The card according to claim 1, the card being flexible.

19. (new) A method of using a card for insertion in a card reader, the card comprising a memory card device having connections forming an input/output means and having embedded on the card a microprocessor and a plurality of memory units external to the microprocessor for storage of digital data, the method comprising:

selecting one of the memory units;

routing address information and data to and/or from the selected memory unit;

routing through the microprocessor all data and address information sent to and sent by the card; and

supplying address information from the microprocessor relating to the data sent to each selected memory unit.